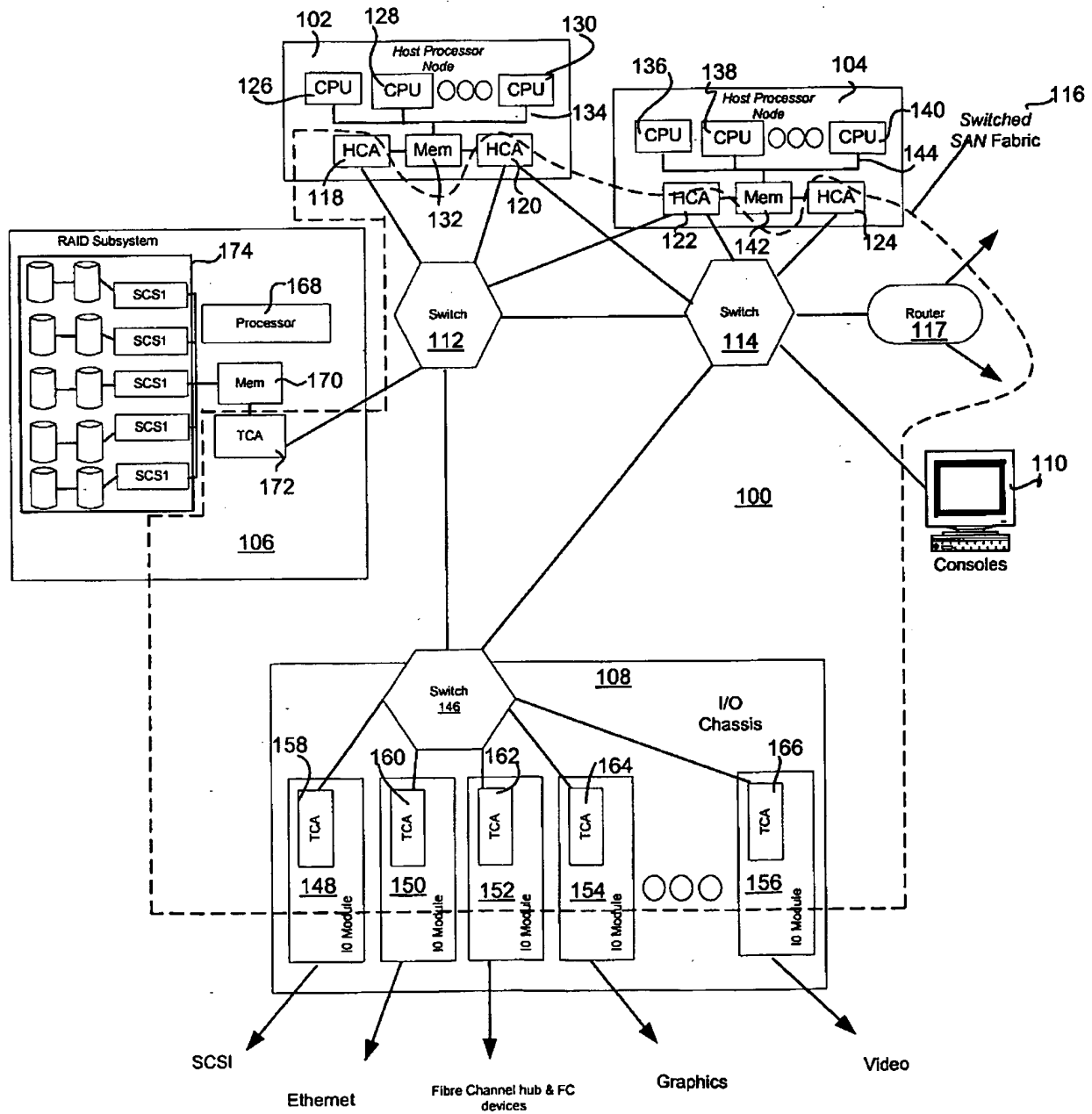


Figure 1

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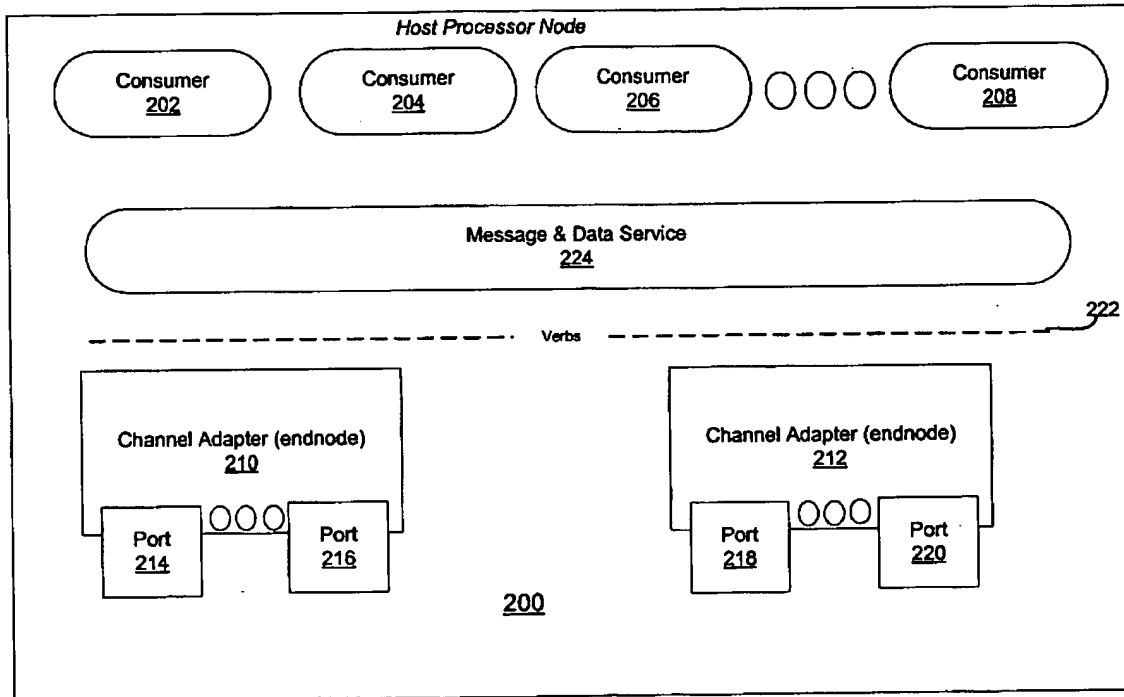


Figure 2

The diagram illustrates a data transfer system. At the top, a rounded rectangle labeled **Memory 340** contains a sequence of blocks: **302**, **304**, **306**, **308**, and **310**. Each of these blocks has a 'P' in a circle on its left side. Below this sequence is a block labeled **DMA 340**. To the right of the DMA 340 is a **Channel Adapter** containing two ovals labeled **SMA 336** and **MTP 338**. Below the Channel Adapter are three **Port** blocks: **Port 312**, **Port 314**, and **Port 316**. Each port block contains a sequence of blocks labeled **318**, **320**, **322**, **324**, **326**, and **328**. Each of these blocks has a 'V' in a circle on its left side. A line labeled **300** connects the DMA 340 to Port 312. A line labeled **330** connects the Channel Adapter to Port 316. A line labeled **332** connects the Channel Adapter to Port 314. A line labeled **334** connects the Channel Adapter to Port 312. A line labeled **Transport** connects Port 312 to Port 314. A line labeled **336** connects Port 314 to Port 316.

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Figure 4

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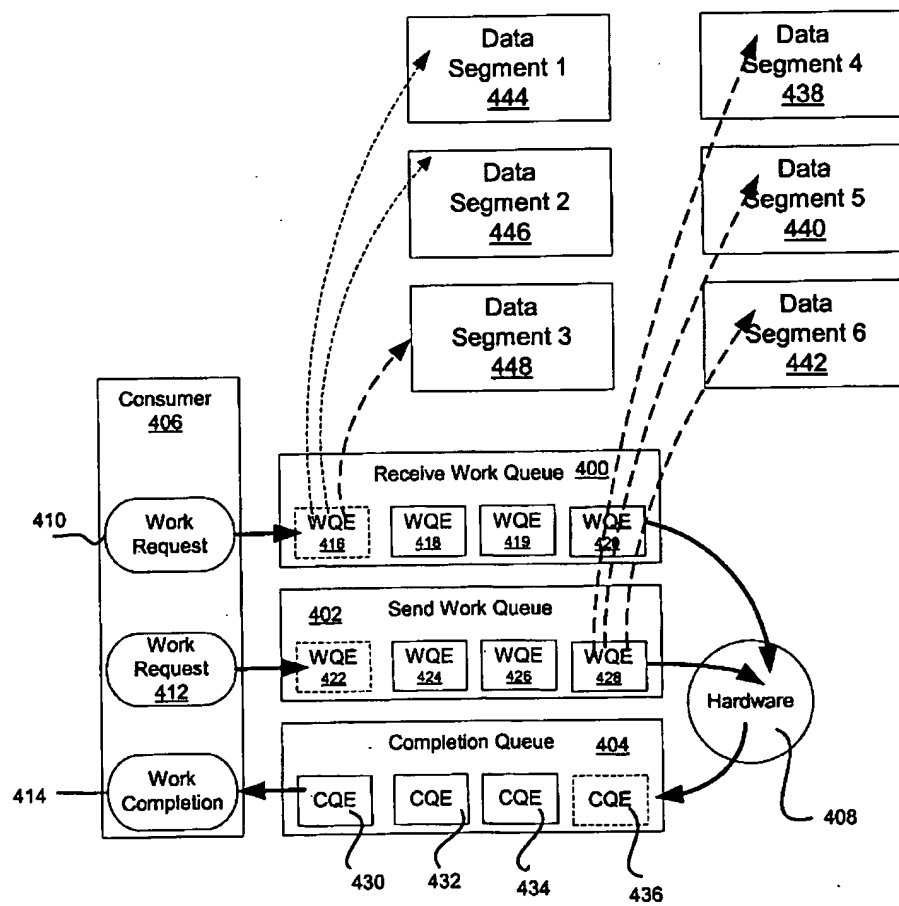


Figure 5

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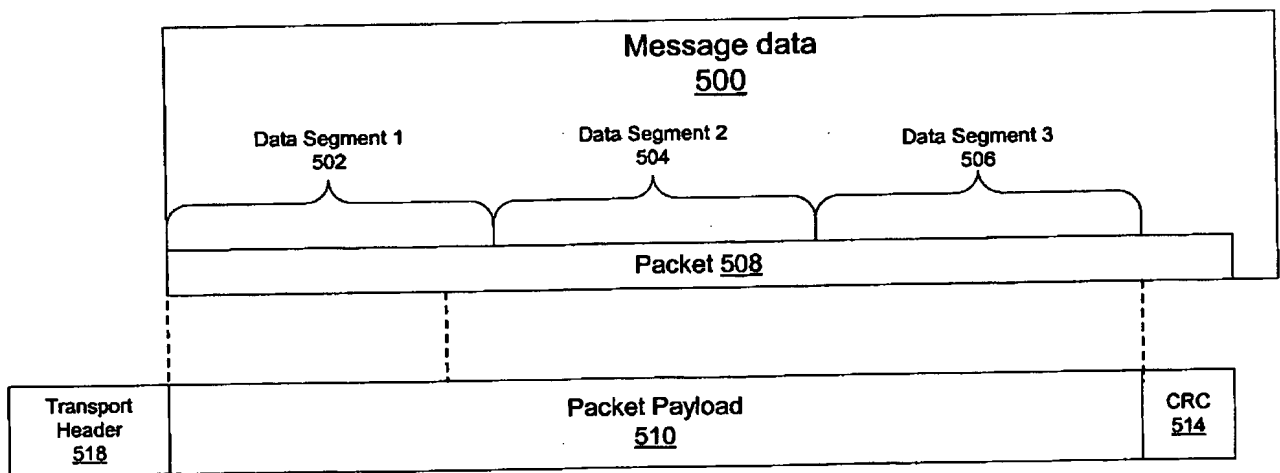
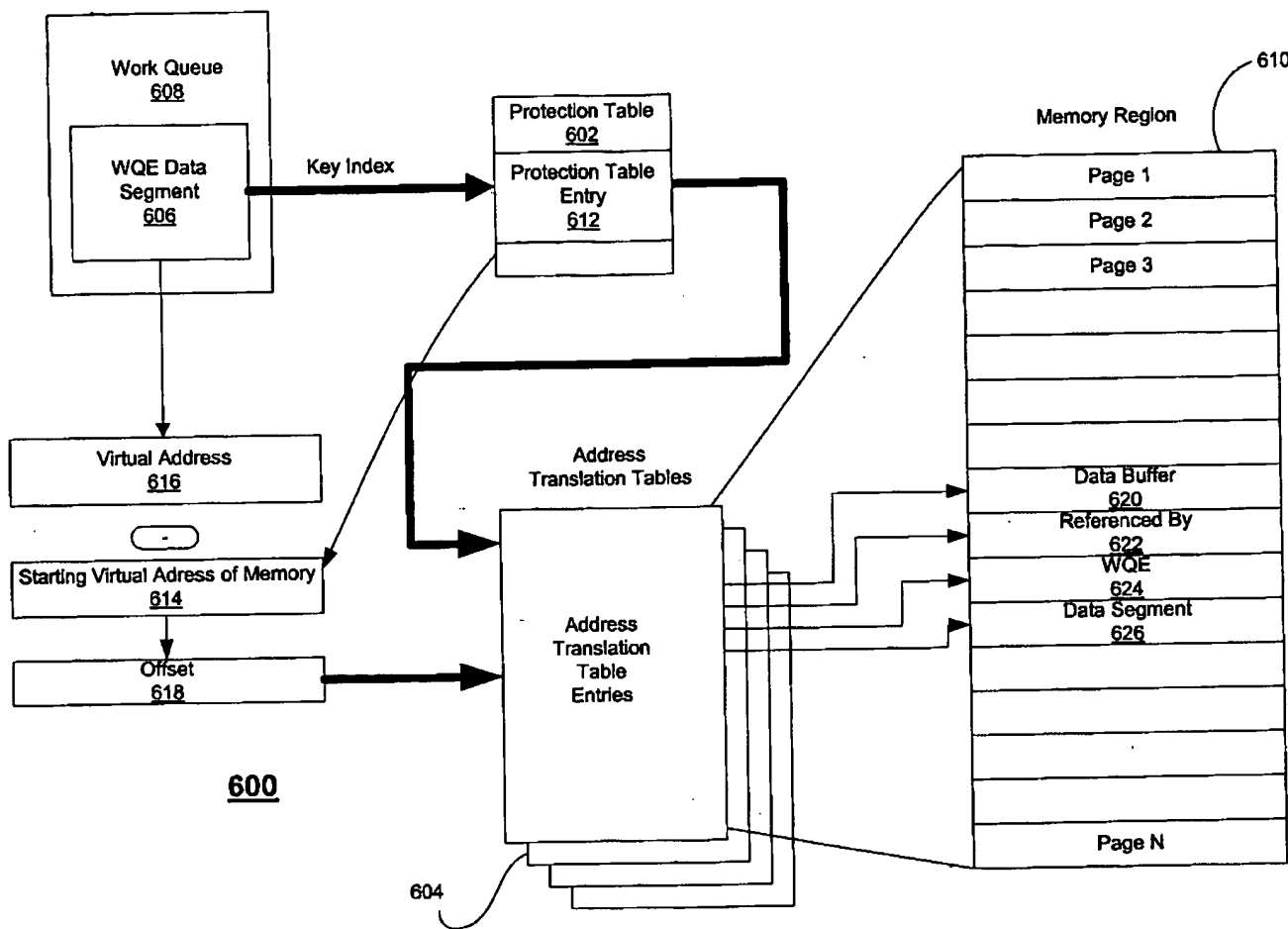


Figure 6

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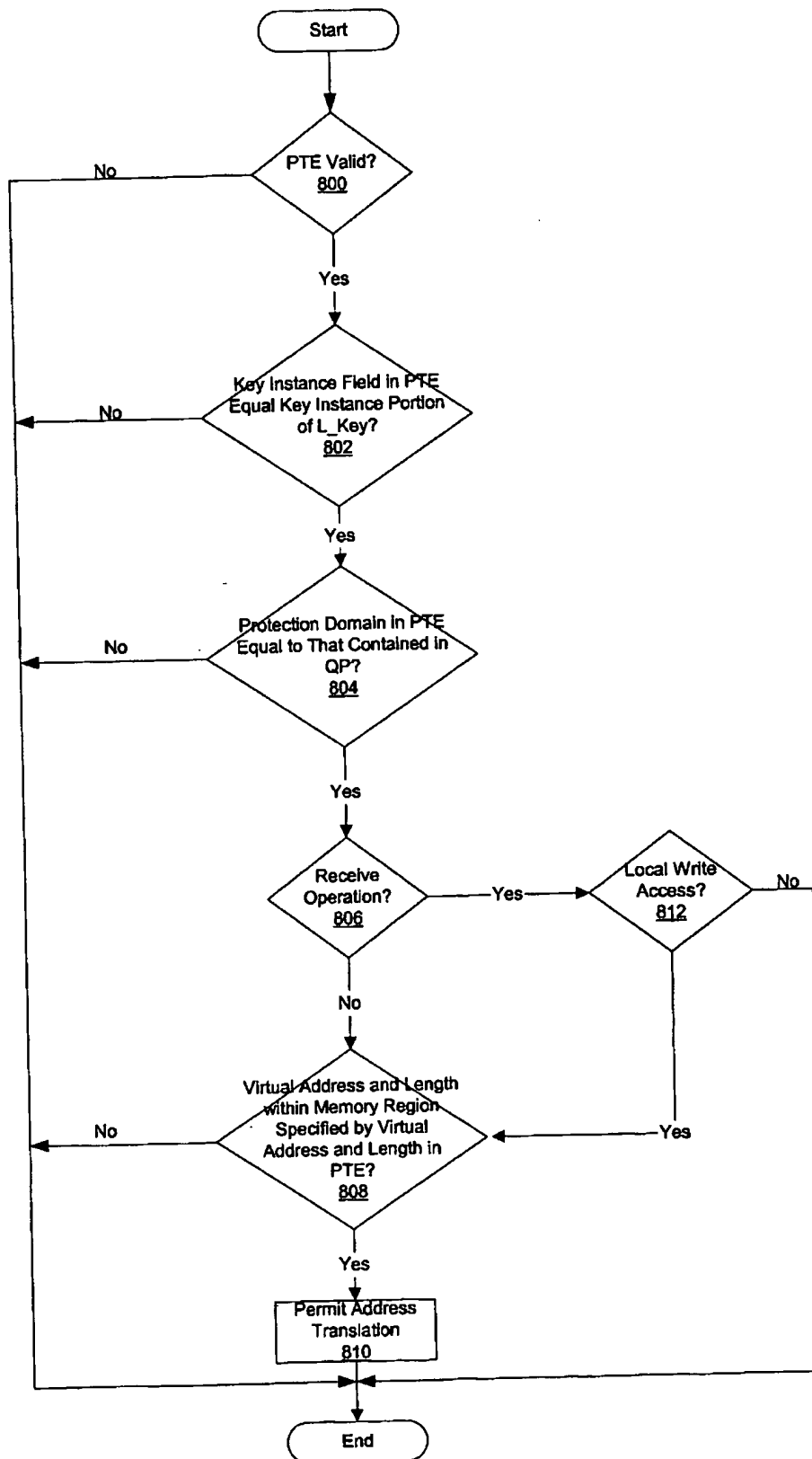
Protection Table Entry

Virtual Address of Start of Memory Region	<u>702</u>
Length of Memory Region	<u>704</u>
Protection Domain	<u>706</u>
Local and Remote Access Control	<u>708</u>
Key_Instance	<u>710</u>
Address Translation Pointer	<u>712</u>

[illegible]

Figure 8

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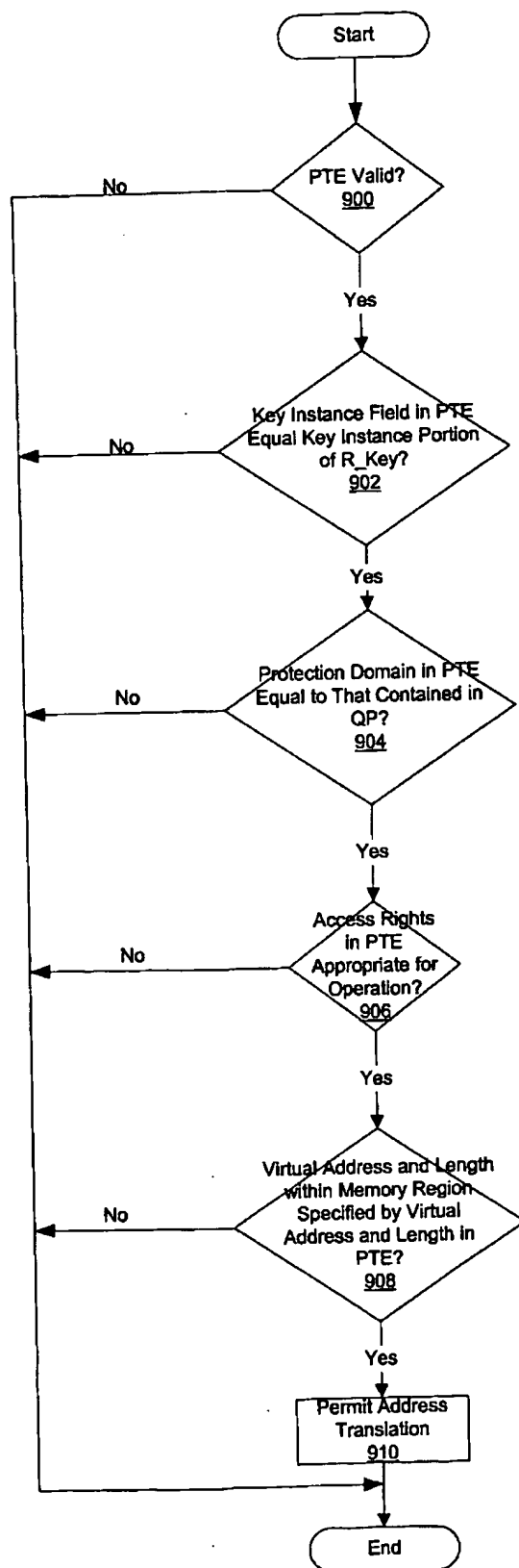


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[illegible]